ACKNOWLEDGEMENT RECEIPT

Title of Invention	A SYSTEM AND METHOD OF PROVIDING ERROR DETECTION AND CORRECTION CAPABILITY IN AN INTEGRATED CIRCUIT USING REDUNDANT LOGIC CELLS OF AN EMBEDDED FPGA

Submission Type : Utility Patent Filing

Application Number:

10/709754

EFS ID: 61574

Server Response:

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ISVR1	Submission was successfully submitted - Even if Informational or Warning Messages appear below, please do not resubmit this application
ICON1	3753
WGEN24	Filename= all XMLs having <invention-title> BusinessRule= Common elements across multiple XML documents within the submission package do not have synchronized data content. #Supporting Msg:<invention-title> not the same or having font fomat.

First Named Applicant: JOHN COHN

Attorney Docket Number: BUR920040001US1

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File Listing:

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us-request	BUR920040001US1-usrequ.xml	3964	2004-05-26
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package-data	us-package-data.xsl	19263	2004-05-26
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